

FIG. 1 (prior art)

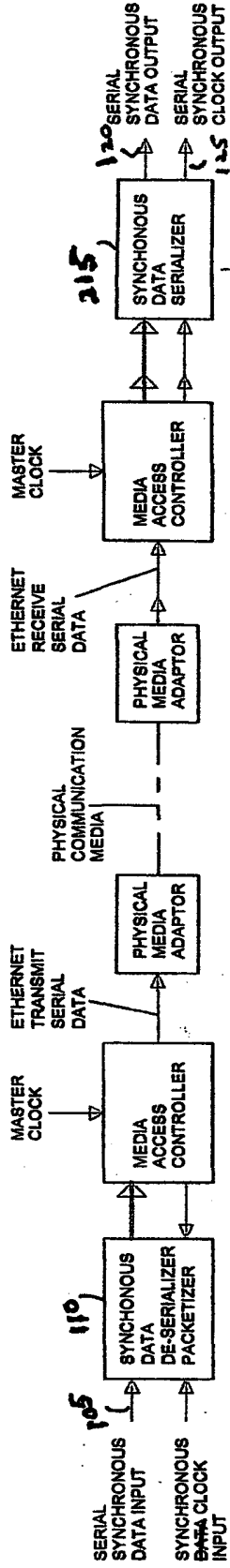


Figure 1, Prior art IDM signal-over-Ethernet installation

①

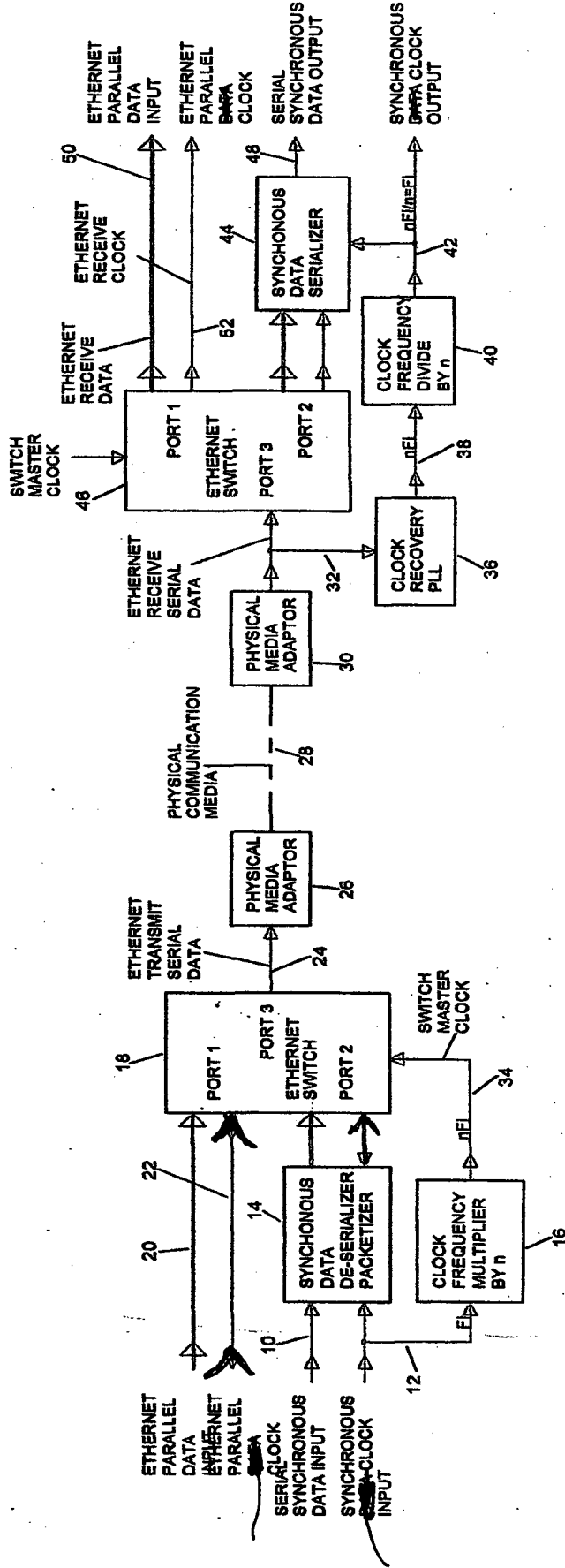


Figure 2, An embodiment of the invention in one direction of transmission

Fig. 2(b)

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 PCT/INT 860421-0001

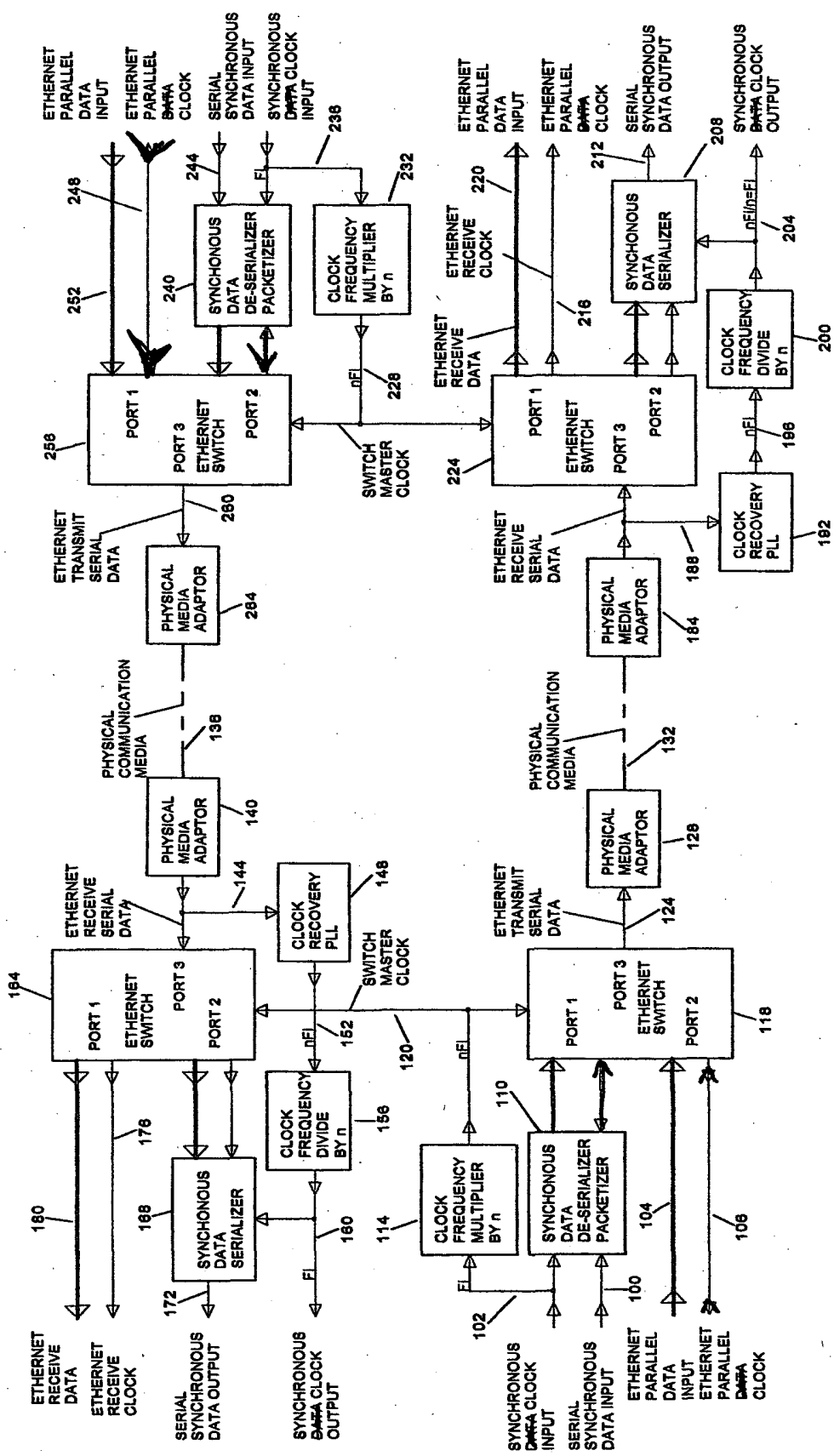


Figure 3. An embodiment of the invention in bi-directional transmission of TDM signals over Ethernet.

FIG. 3

3

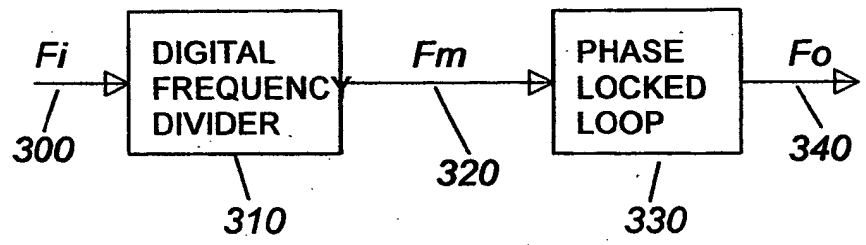


Figure 4, An embodiment of a frequency Multiplier / Divider.

FIG. 4

4

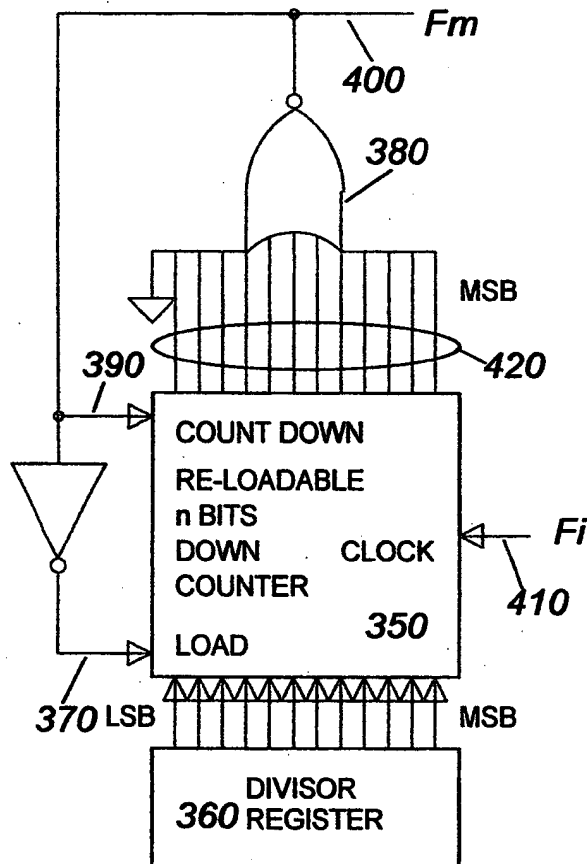


Figure 5, An embodiment of a Digital Frequency Divider.

FIG. 5

5

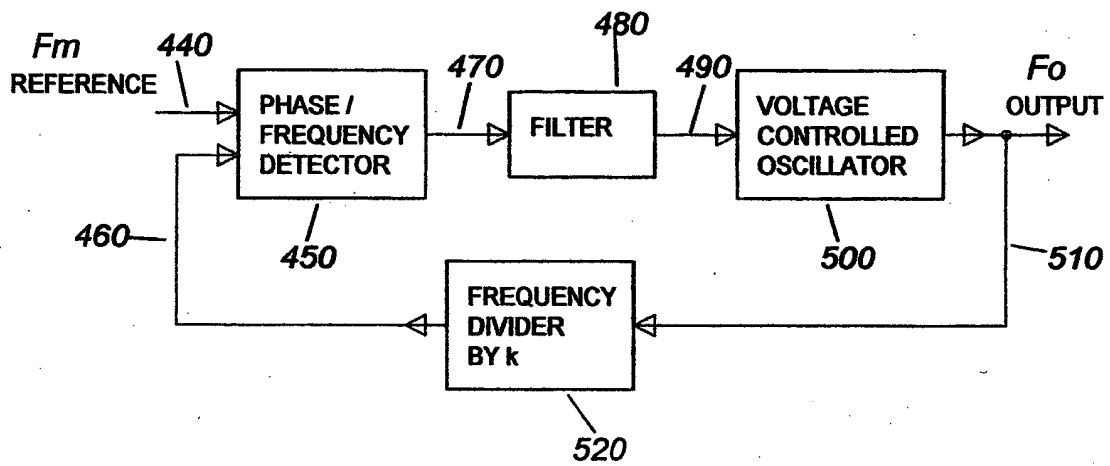


Figure 6, An embodiment of a Phase Locked Loop.

FIG. 6

6

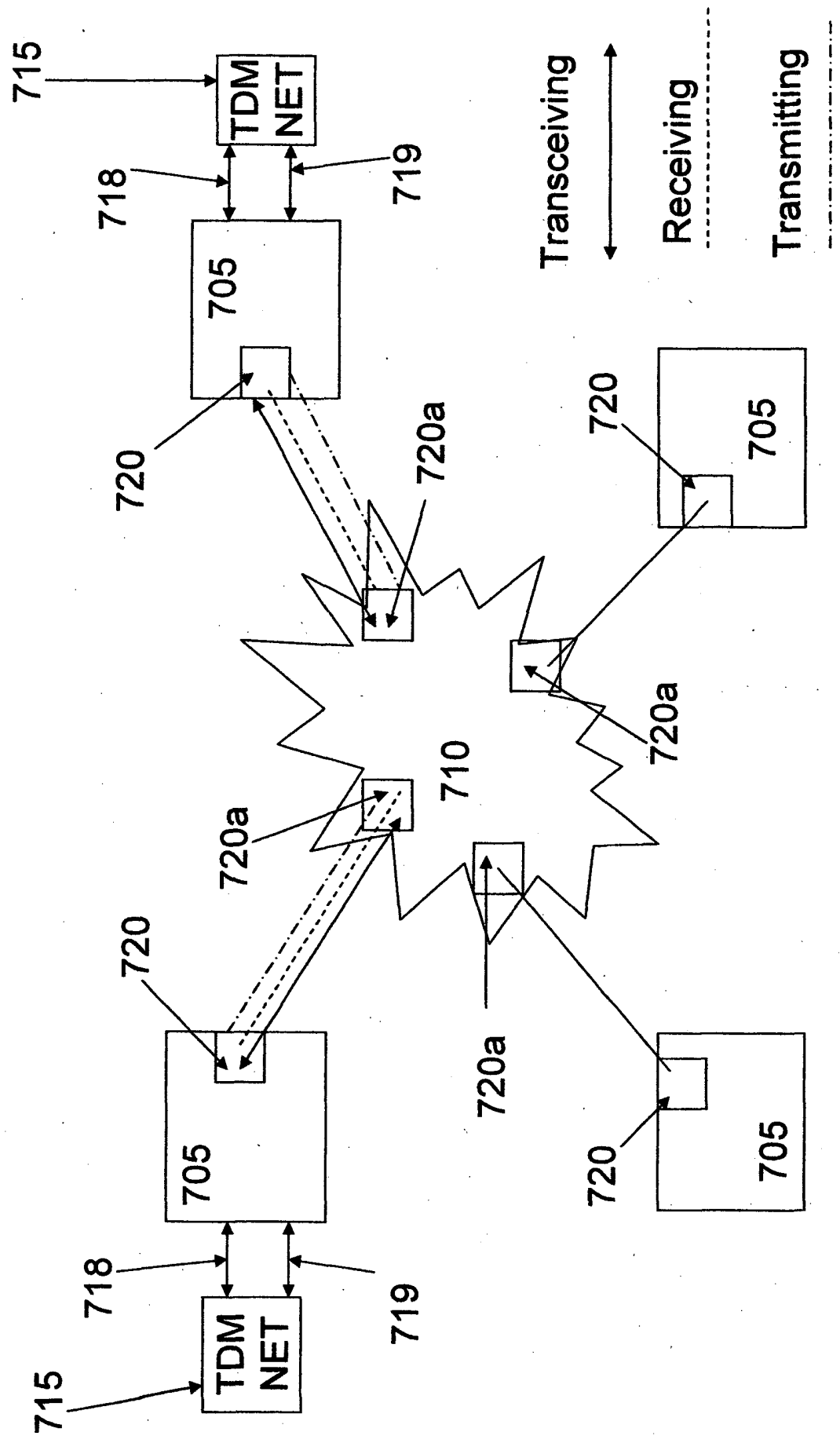


Fig. 7

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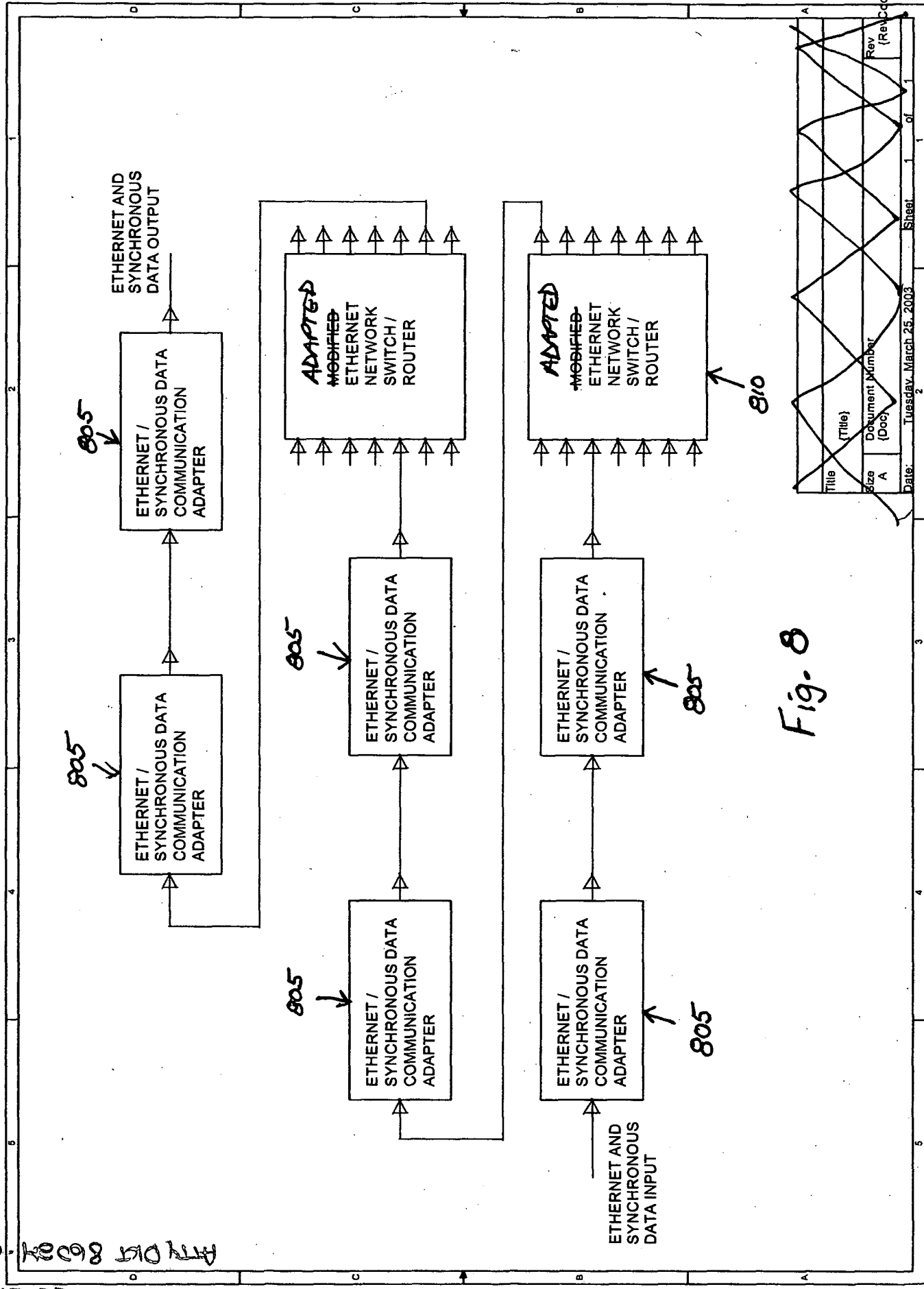


Fig. 8

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FIG. 9  
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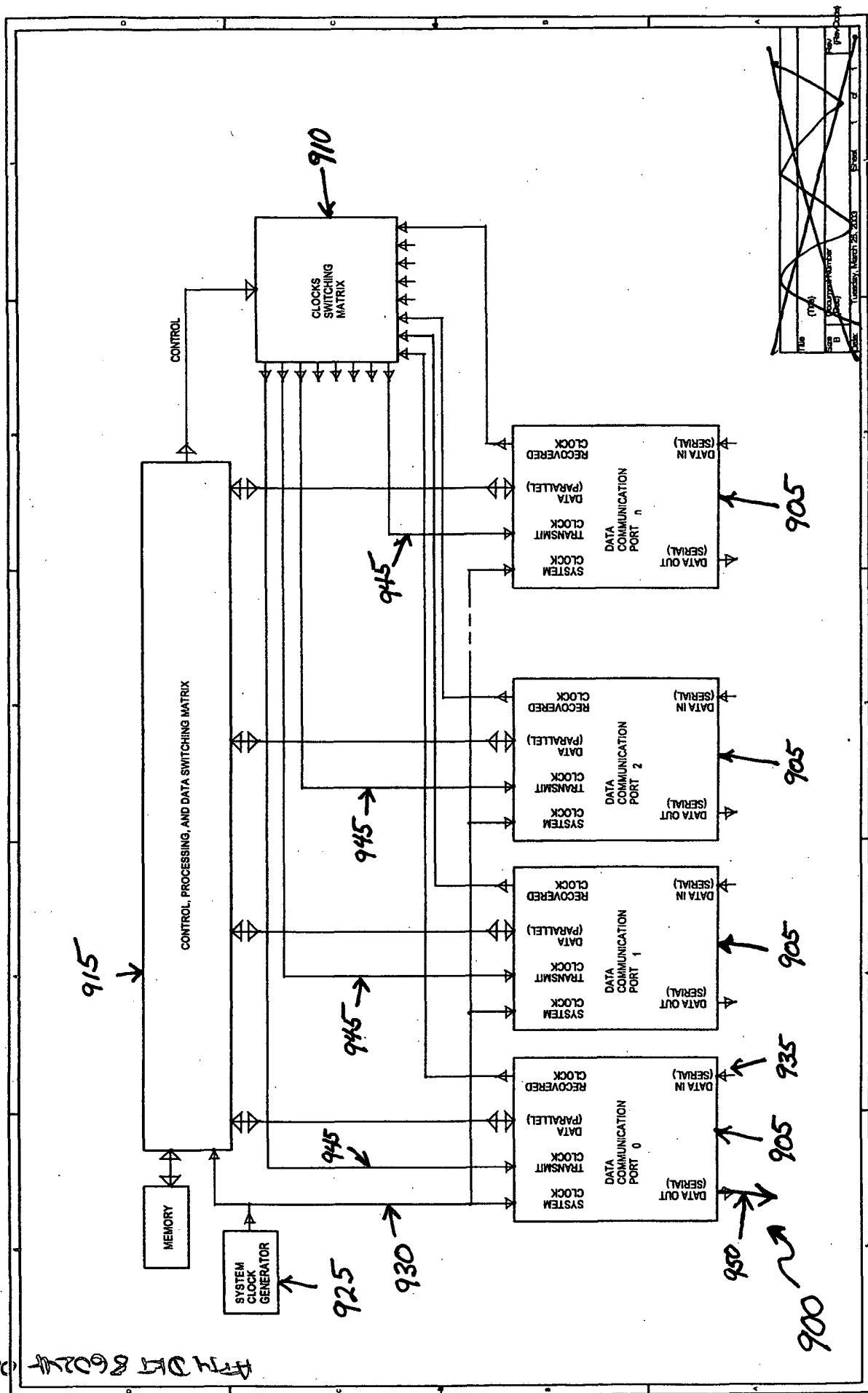


Fig. 9